**Version No.:** 

01

# **SPECIFICATIONS**

PRODUCT: LCD MODULE

MODEL NO.: \$60779

	CUSTOMER		SUCCESS			
APPROVED	CHECKED	CHECKED	APPROVED	CHECKED	PREPARED	

□ APPROVED FOR SPECIFICATIONS PRELIMINARY

■ APPROVED FOR SPECIFICATIONS AND SAMPLE

深圳市宇顺电子有限公司 SUCCESS ELECTRONIC CO., LTD.

STANDARD DOC.	PRODUCT SPEC.	MODULE NO.	S60779	PAGE	1
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# **RECORDS OF REVISION**

DATE	REVISED NO.	REVISED DESCRIPTIONS	PREPARED	CHECKED	APPROVED
2008-4-8	01	FIRST ISSUE	CWF		

STANDARD DOC.	PRODUCT SPEC.	MODULE NO.	S60779	PAGE	2				
	CONTENTS								
1.	1. GENERAL SPECIFICATIONS								
2.	FEATURES			3					
3.	MACHANICAL	SPECIFICAT	TIONS	3					
4.	OUTLINE DIM	ESIONS		4	,				
5.	INTERFACE AS	SSIGNMENT		5	;				
6.	APPLICATION	CIRCUIT -		6					
7.	BLOCK DIAGE	RAM		6					
8.	TIMING CHAR	ACTERISTIC	cs	7	,				
9.	RESET TIMINO	G CHARACTI	ERISTICS	8	1				
10.	POWER ON/OF	FF SEQUENC	Е	8	1				
11	INITIALIZED (	CODE		10	0				
12.	INSTRUCTION	TABLE		1	1				
13.	DDRAM ARRA	NGMENT -		12	2				
14.	ABSOLUTE MA	AXIMUM RA	ΓINGS ·····	1.	3				
15.	ELECTRICAL (	CHARACTE	RISTICS	1.	3				
16.	LED BACKLIG	НТ		14	4				
17.	OPTICAL CHA	RACTERIST	ICS	1:	5				
18.	ENVIRONMEN	TALABSOLU	UTE MAXIMUM RATINGS	1′	7				
19.	RELIABILITY			1′	7				
20.	THE STANDAR	RD OF INSPEC	CTION	1′	7				
21.	USING LCD MO	ODULES		2	1				
		SUCCES	S ELECTRONIC CO.,LTD.						

STANDARD DOC.	PRODUCT SPEC.	MODULE NO.	S60779	PAGE	3
------------------	------------------	---------------	--------	------	---

#### 1. GENERAL SPECIFICATIONS

#### 1-1 SCOPE:

This specification covers the delivery requirements for the liquid crystal display delivered by SUCCESS ELECTRONIC to Customer

#### 1-2 PRODUCTS:

Liquid Crystal Display Module (LCM)

#### 1-3 MODULE NAME:

S60779

#### 2. FEATURES

#### 2-1 MAIN LCD (LARGE)

(1) Display Type: STN(Y/G), Positive, Transmissive, 6 o'clock

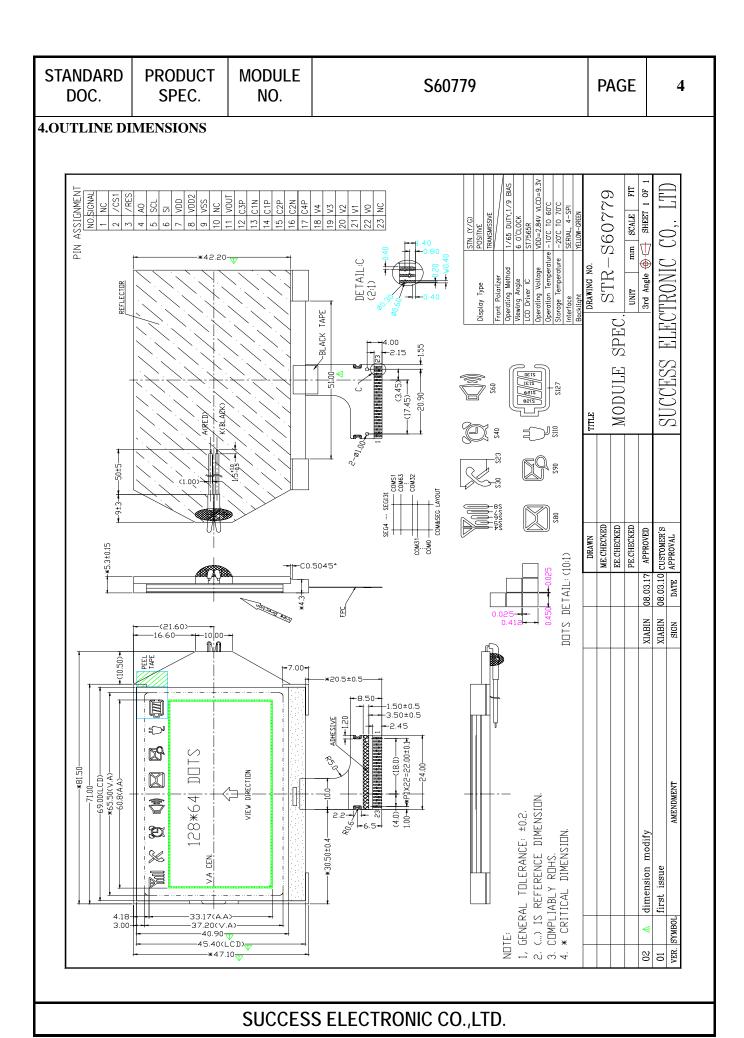
(2) Driving Method: 1/65duty, 1/9 bias

(3) Built-in controller: ST7565R

(4) With Y/G LED Backlight

#### 3. MACHANICAL SPECIFICATIONS

ITEM	SPECIFICATIONS	UNIT
OUTLINE DIMEMSIONS	81.5(W) × 47.1(H) ×5.3 (T)	mm
VIEWING AREA	65.5(W) ×37.2(H)	mm
ACTIVE AREA	60.8(W) ×33.17(H)	mm
DISP.CONSTRUCTION	128× 64 Dots + ICONS	
NUMBER OF DOTS	128× 64	Dots
DOT SIZE	0.45 (W) × 0.412 (H)	mm
DOT PITCH	0.475(W) × 0.437(H)	mm
ASSY.TYPE	COG+B/L	
BACKLIGHT	Yellow-Green	_
WEIGHT	TBD	g



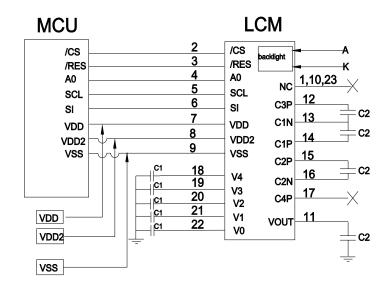
STANDARD DOC.	PRODUCT SPEC.	MODULE NO.	S60779	PAGE	5
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# 5. INTERFACE ASSIGNMENT

PIN NO.	FUNCTION DESCRIPTIONS	SYMBOL
1	NC	NC
2	This is the chip select signal.	/CS1
3	When /RES is set to "L," the settings are initialized.	/RES
J	The reset operation is performed by the /RES signal level.	/INLO
	This is connect to the least significant bit of the normal MPU address bus,	
4	and it determines whether the data bits are data or a command.	A0
	A0 = "H": Indicates that D0 to D7 are display data.	7.0
	A0 = "L": Indicates that D0 to D7 are control data.	
5	When the 4-line SPI interface is selected (P/S = "L"):	SCL
6	SI: 4-line SPI data input; SCL: the 4-line SPI clock input.	SI
7	Power supply for logic	VDD
8	These are the power supply pads for the step-up voltage circuit for the LCD.	VDD2
9	Ground	VSS
10	NC	NC
11	DC/DC voltage converter. Connect a capacitor between this terminal and VSS or VDD	VOUT
12	DC/DC voltage converter.	C3P
13	DC/DC voltage converter.	C1N
14	DC/DC voltage converter.	C1P
15	DC/DC voltage converter.	C2P
16	DC/DC voltage converter.	C2N
17	DC/DC voltage converter.	C4P
18	This is a multi-level power supply for the liquid crystal drive. The voltage Supply applied is	V4
19	determined by the liquid crystal cell, and is changed through the use of a resistive voltage	V3
20	divided or through changing the impedance using an op. amp. Voltage levels are	V2
21	determined based on Vss, and must maintain the relative magnitudes shown below.	V1
22	V0 ≧V1 ≧V2 ≧V3 ≧V4 ≧Vss	V0
23	NC	NC

STANDARD DOC.	PRODUCT SPEC.	MODULE NO.	S60779	PAGE	6
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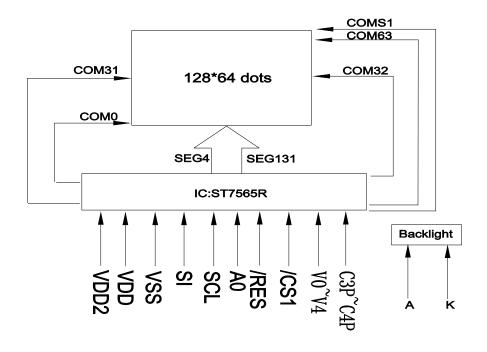
#### **6.APPLICATION CIRCUIT**

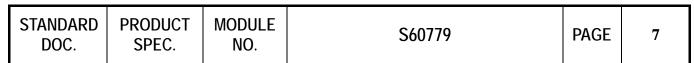


#### Remark:

**1.** Reference value: C1: 0.1~4.7UF /16V; C2:1.0~4.7 UF / 16V

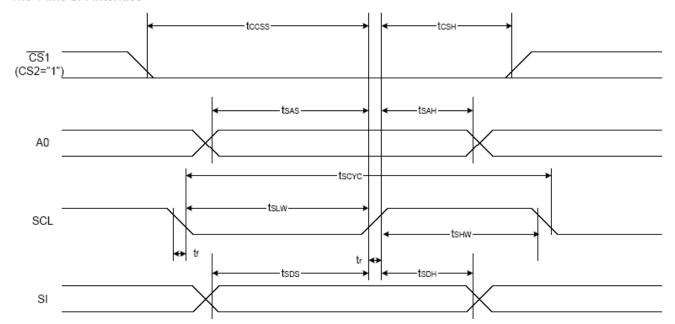
#### 7. BLOCK DIAGRA





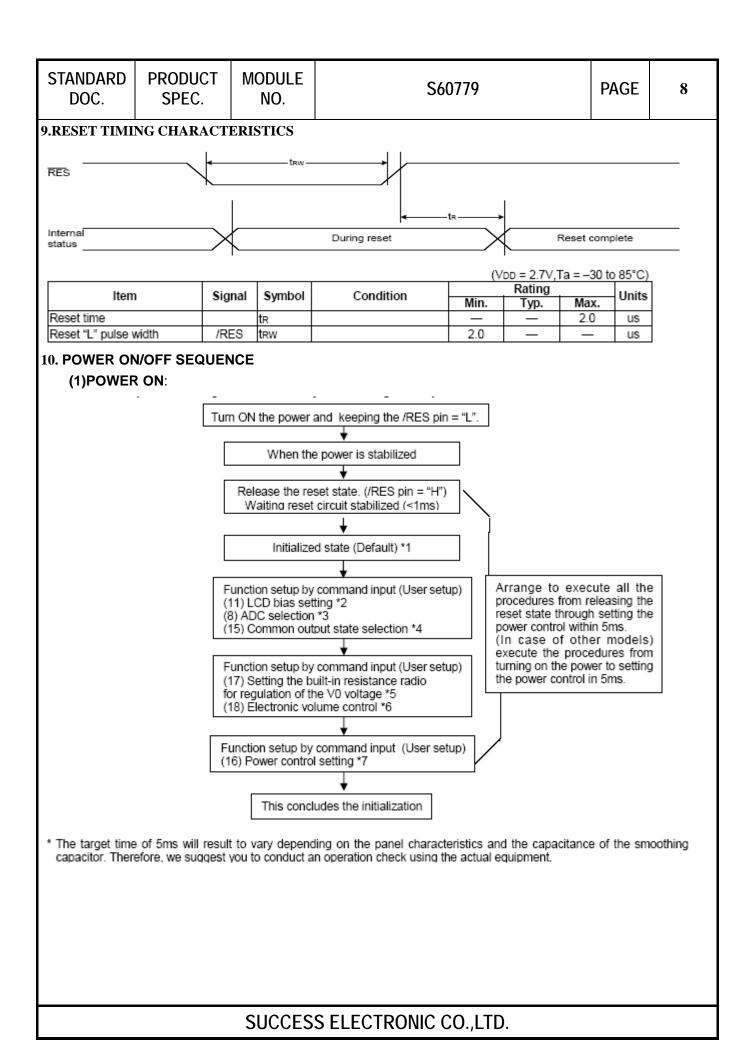
#### 8.TIMING CHARACTERISTICS

The 4-line SPI Interface

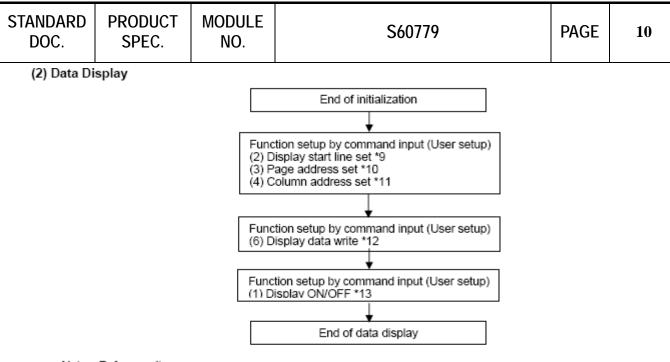


(VDD = 2.7V, Ta = -30 to 85°C)

Item	Cianal	Symbol	Condition	Rating		Units	
item	Signal	Symbol	Condition	Min.	Max.	Ullits	
4-line SPI Clock Period		Tscyc		100	_		
SCL "H" pulse width	SCL	Tshw		50	_		
SCL "L" pulse width		Tslw		50	_		
Address setup time	- A0	Tsas		30	_		
Address hold time	A0	Тѕан		20	_	ns	
Data setup time	- SI	Tsps		30	_		
Data hold time	] 51	Тѕон		20	_		
CS-SCL time	- cs	Tcss		30	_		
CS-SCL time		Тсѕн		60	_		



STANDARD DOC.	PRODUCT SPEC.	MODULE NO.	S60779	PAGE	9
*1: Descrip *2: Comma *3: Comma *4: Comma *5: Descrip the V0 *6: Descrip	voltage otion of functions; F	Resetting circuit CD bias setting DC selection ommon output st Power circuit & C		dio for regul	ation of
		SUCCES	S ELECTRONIC CO.,LTD.		

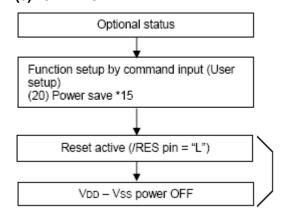


Notes: Reference items

- \*9: Command Description; Display start line set
- \*10: Command Description; Page address set
- \*11: Command Description, Column address set
- \*12: Command Description, Display data write
- \*13: Command Description; Display ON/OFF

Avoid displaying all the data at the data display start (when the display is ON) in white.

#### (3)POWER OFF:



Set the time (t<sub>L</sub>) from reset active to turning off the V<sub>DD</sub> - V<sub>SS</sub> power (V<sub>DD</sub> - V<sub>SS</sub> = 1.8V) longer than the time (t<sub>H</sub>) when the potential of V<sub>4</sub>  $\sim$  V<sub>0</sub> becomes below the threshold voltage (approximately 1V) of the LCD panel. For t<sub>H</sub>, refer to the <Reference Data> of this event. When t<sub>H</sub> is too long, insert a resistor between V<sub>0</sub> and V<sub>SS</sub> to reduce it.

Notes: Reference items

- \*14: The logic circuit of this IC's power supply Vob Vss controls the driver of the LCD power supply Vss Vo. So, if the power supply Vbb Vss is cut off when the LCD power supply Vss Vo has still any residual voltage, the driver (COM. SEG) may output any uncontrolled voltage. When turning off the power, observe the following basic procedures:
  - After turning off the internal power supply, make sure that the potential Vo ~ V4 has become below the threshold voltage of the LCD panel, and then turn off this IC's power supply (Vob - Vss). 6. Description of Function, 6.7 Power Circuit
- \*15: After inputting the power save command, be sure to reset the function using the /RES terminal until the power supply Vop - Vss is turned off. 7. Command Description (20) Power Save
- \*16: After inputting the power save command, do not reset the function using the /RES terminal until the power supply Vpp - Vss is turned off. 7. Command Description (20) Power Save

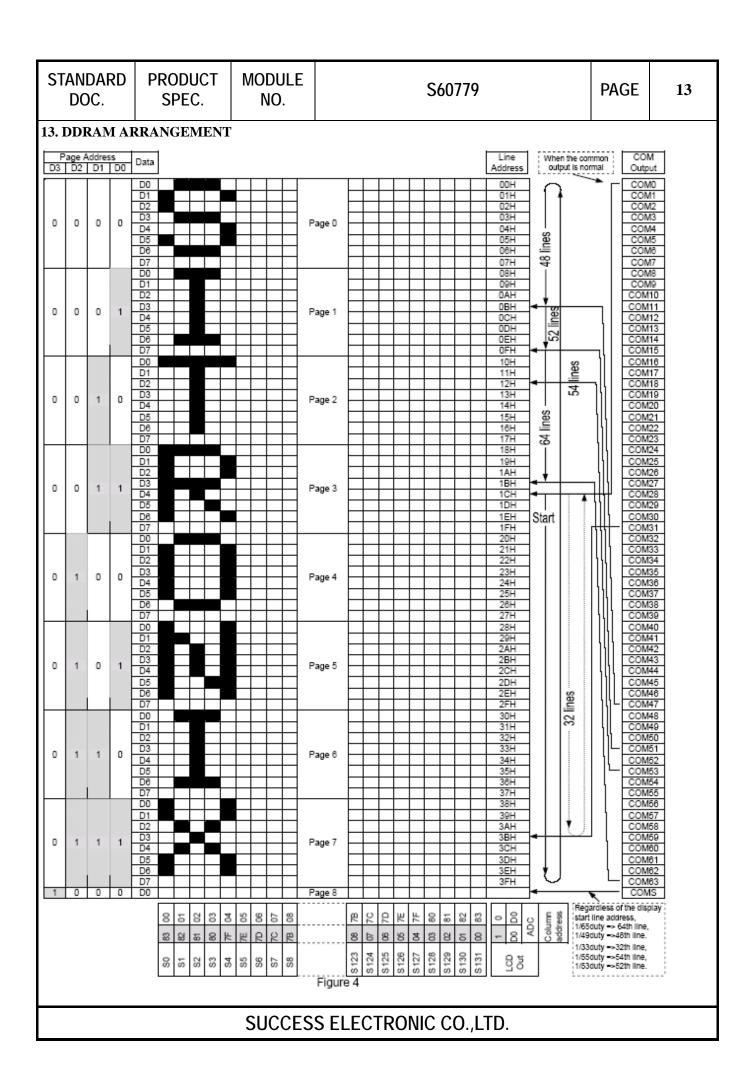
STANDARD PRODUCT MODULE NO. S60779 PAGE 11

```
11. INITIALIZED CODE
void main(void)
 P2=0xff;
  vopcode=52;//VOP
  Ra_Rb=0x23;
  LCMSEG=128;
 LCMCOM=64;
  interface=1;
                                        /* 1/9 bias */
  writec(0xa2);
  writec(0xD1);
                                        /* icon on */
                                        /* ADC select , Normal */
  writec(0xa0);
  writec(0xc8);
                                        /* Common output reverse */
  writec (0xa6);
                                        /* normal display 1=on */
  writec(0x2f);
                                        /* V/C off, V/R off, V/F on */
  writec(Ra_Rb);
                                         /* internal resistor ratio */
  writec(0x81);
                                        /* electronic volume mode set */
  writec(vopcode);
                                           /* electronic volume */
                                        /* display start first line */
  writec(0x40);
  writec(0xaf);
                                        /* display on */
void address (unsigned char page)
{ writec(0x40);
                                        /* display start first line */
  writec(0xb0+page);
                                        /* Page adress set */
  writec(0x10);
                                        /* column address 0 ( High Byte ) */
  writec(0x04);
                                        /* column address 0 (Low Byte) */
```

STANDARD DOC.	PRODUCT SPEC.	MODULE NO.	S60779	PAGE	12
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#### 12. INSTRUCTION TABLE

Command							Coc					Function
Communa	A0	/RD	/WR	D7			D4				D0	
(1) Display ON/OFF	0	1	0	1	0	1	0	1	1	1	0	LCD display ON/OFF 0: OFF, 1: ON
(2) Display start line set	0	1	0	0	1		Displ	ay st	art a	ddre	ss	Sets the display RAM display start line address
(3) Page address set	0	1	0	1	0	1	1	Р	age	addr	ess	Sets the display RAM page address
<ul><li>(4) Column address set upper bit Column address set lower bit</li></ul>	0	1	0	0	0	0	1	co Le		add ignifi	ress cant	Sets the most significant 4 bits of the displa RAM column address. Sets the least significant 4 bits of the displa RAM column address.
(5) Status read	0	0	1		Sta	tus		0	0	0	0	Reads the status data
(6) Display data write	1	1	0					W	rite d	ata		Writes to the display RAM
(7) Display data read	1	0	1					Re	ad d	ata		Reads from the display RAM
(8) ADC select	0	1	0	1	0	1	0	0	0	0	0	Sets the display RAM address SEG output correspondence 0: normal, 1: reverse
(9) Display normal/ reverse	0	1	0	1	0	1	0	0	1	1	. 1	Sets the LCD display normal/ reverse 0: normal, 1: reverse
(10) Display all points ON/OFF	0	1	0	1	0	1	0	0	1	0	0 1	Display all points 0: normal display 1: all points ON
(11) LCD bias set	0	1	0	1	0	1	0	0	0	1	0 1	Sets the LCD drive voltage bias ratio 0: 1/9 bias, 1: 1/7 bias (ST7565R)
(12) Read/modify/write	0	1	0	1	1	1	0	0	0	0	0	Column address increment At write: +1 At read: 0
(13) End	0	1	0	1	1	1	0	1	1	1	0	Clear read/modify/write
(14) Reset	0	1	0	1	1	1	0	0	0	1	0	Internal reset
(15) Common output mode select	0	1	0	1	1	0	0	0	*	*	*	Select COM output scan direction 0: normal direction 1: reverse direction
(16) Power control set	0	1	0	0	0	1	0	1	0	pera mod		Select internal power supply operating mod
(17) Vo voltage regulator internal resistor ratio set	0	1	0	0	0	1	0	0	Res	sistor	ratio	Select internal resistor ratio(Rb/Ra) mode
(18) Electronic volume mode set Electronic volume	0	1	0	1	0			0		0	. 1	Set the Vo output voltage electronic volume register
register set				0	0		lectro				ilue 	
(19) Static indicator ON/OFF Static indicator	0	1	0	1	0	1	0	1	1	0	0 1	0: OFF, 1: ON
register set				0	0	0	0	0	0	0	Mode	Set the flashing mode
(20) Booster ratio set	0	1	0	1	1	1	1	1	0	0	0	select booster ratio 00: 2x,3x,4x
(25) Dooster Tallo set	"	'	J	0	0	0	0	0	0		p-up alue	01: 5x 11: 6x
(21) Power save	0	1	0									Display OFF and display all points ON compound command
(22) NOP	0	1	0	1	1	1	0	0	0	1	1	Command for non-operation
(23) Test	0	1	0	1	1	1	1	*	*	*	*	Command for IC test. Do not use this command



STANDARD F	PRODUCT SPEC.	MODULE NO.	S60779	PAGE	14
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# 14. ABSOLUTE MAXIMUM RATING

ITEM	SYMBOL	CONDITION	STA	UNIT		
I I LIVI	STWIDOL	CONDITION	MIN	TYP	MAX	OINIT
POWER SUPPLY FOR LOGIC	VDD-VSS	Ta=25°C	0.3	_	3.6	V
POWER SUPPLY FOR LCD DRIVING	V0-VSS	Ta=25°C	0.3	_	13.5	V
INPUT VOLTAGE	V1~V4	Ta=25°C	-0.3	_	V0	V
OPERATION TEMPERATURE	TOPR		-10	_	60	$^{\circ}\mathbb{C}$
STORAGE TEMPERATURE	TSTG		- 20	1	70	$^{\circ}\mathbb{C}$

NOTES:

(1) LCM should be grounded during handling LCM.

### 15. ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITIONS	STAN	LUE	- UNIT		
I I LIVI	STINIDOL	CONDITIONS	MIN	TYP	MAX	OIVII	
POWER SUPPLY VOLTAGE	VDD-VSS	Ta= +25°C		2.84		V	
POWER SUPPLY FOR LCD DRIVING	V0-VSS	Ta= +25°C	8.70	8.90	9.10	V	
INPUT VOLTAGE "H" LEVEL	VIH	_	0.8VDD	_	VDD	V	
INPUT VOLTAGE "L" LEVEL	VIL	_	VSS	_	0.2VDD	V	
OUTPUT VOLTAGE "H" LEVEL	VOH	IOH=-0.5uA	0.8VDD		VDD	٧	
OUTPUT VOLTAGE "L" LEVEL	VOL	IOL=-0.5uA	VSS	_	0.2VDD	V	

STANDARD DOC.	PRODUCT SPEC.	MODULE NO.	S60779	PAGE	15
16. LED BAC	KLIGHT				

16-1 POWER SUPPLY FOR LED BACKLIGHT



#### 16-2 ABSOLUTE MAXIMUN RATING

PARAMETER	SYMBOL	SPECIFICATIONS	UNIT
POWER DISSIPATION	PD	75	mW
FORWARD CURRENT	IFm	15	mA
REVERSE VOLTAGE	VR	5	V
OPERATION TEMPERATURE	TOPR	-10℃ ~60℃	$^{\circ}$ C
STORAGE TEMPERATURE	TSTG	-20℃ ~+70℃	$^{\circ}\!\mathbb{C}$

#### 16-3 ELECTRICAL CHARACTERISTICS

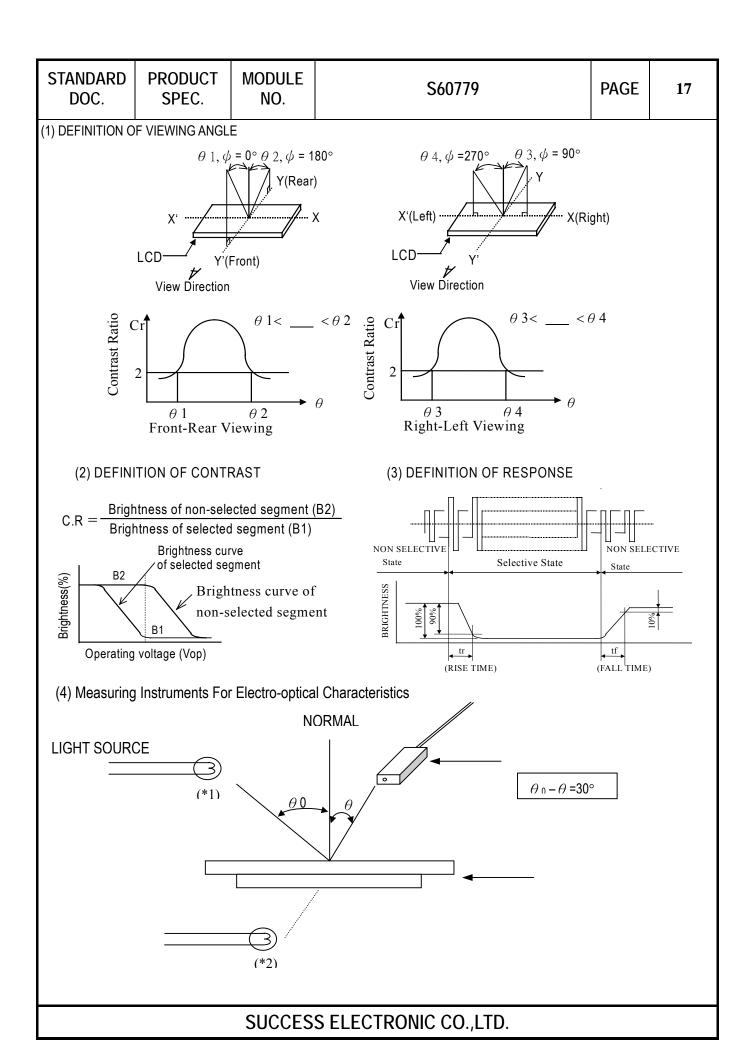
PARAMETER	SYMBOL	LIGHT	CONDITI	STA	- UNIT		
PAIVAIVIETEIX	STWIDOL	SOURCE	ONS	MIN	TYP	MAX	ONIT
PARAMETER VOLTAGE	VAK	Y/G	IF=12.5mA	1.95	2.05	2.15	٧
LUMINOUS INTENSITY	lv	Y/G		1.0	2.5	4.0	cd/m <sub>2</sub>
PEAK EMISSION WAVELENGTH		Y/G	IF=12.5mA	569		575	nm

STANDARD DOC.	PRODUCT SPEC.	MODULE NO.	S60779	PAGE	16
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# 17. OPTICAL CHARACTERISTICS

for LCD 1/65 Duty, 1/9 Bias

Item	)	Symbol	Temp.	Condition	Min.	Тур.	Max.	Unit.	Note	
	Rise	tr	-20°C			1130				
Response	time	u	<b>25</b> ℃	θ=0°		280		mS		
Time	Decay	td	-20°C	φ=0°		1080		1110		
	time	ια	<b>25</b> ℃			220				
				Ф=0°		35				
Viewing Angl	е	θ	<b>25</b> ℃	Ф=90°		35		deg.		
(6:00 H)		H H	₩	K≥2	Ф=180°		30		ueg.	
				Ф=270°		35				
Uniformity		ΔBn	25℃	$\theta = \phi = 0^{\circ}$	70%					
Contrast Rat	io	K	25℃	$\theta = \phi = 0^{\circ}$		8				
	White	X						i		
	Willie	Y						1		
Color of	Red	X						i		
Color of CIE(1931)	Reu	Y						ı		
coordinate	Green	X	25℃	$\theta = \phi = 0^{\circ}$				-		
Coordinate	Green	Y						-		
	Blue	X						-		
	Dide	Y						•		
NTSC Ratio	S				-		-	-		



STANDARD DOC.	PRODUCT SPEC.	MODULE NO.	S60779	PAGE	18
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#### 18. ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

ITEM	SYMBOL	CONDITIONS	CRITERION
OPERATING TEMPERATURE	TOPR	-10°C ∼+60°C	NO DEFECT IN DISPLAYING AND
OFERATING TEMPERATURE	IOFK	-10 ( ) 9 +00 (	OPERATIONAL FUNCTION
STORAGE TEMPERATURE	TSTG	-20°C ∼+70°C	NO DEFECT IN DISPLAYING AND
STORAGE TEMPERATURE	1310	-20 ( ) 9 + 70 (	OPERATIONAL FUNCTION
HUMIDITY	1		WITHOUT CONDENSATION

#### 19. RELIABILITY

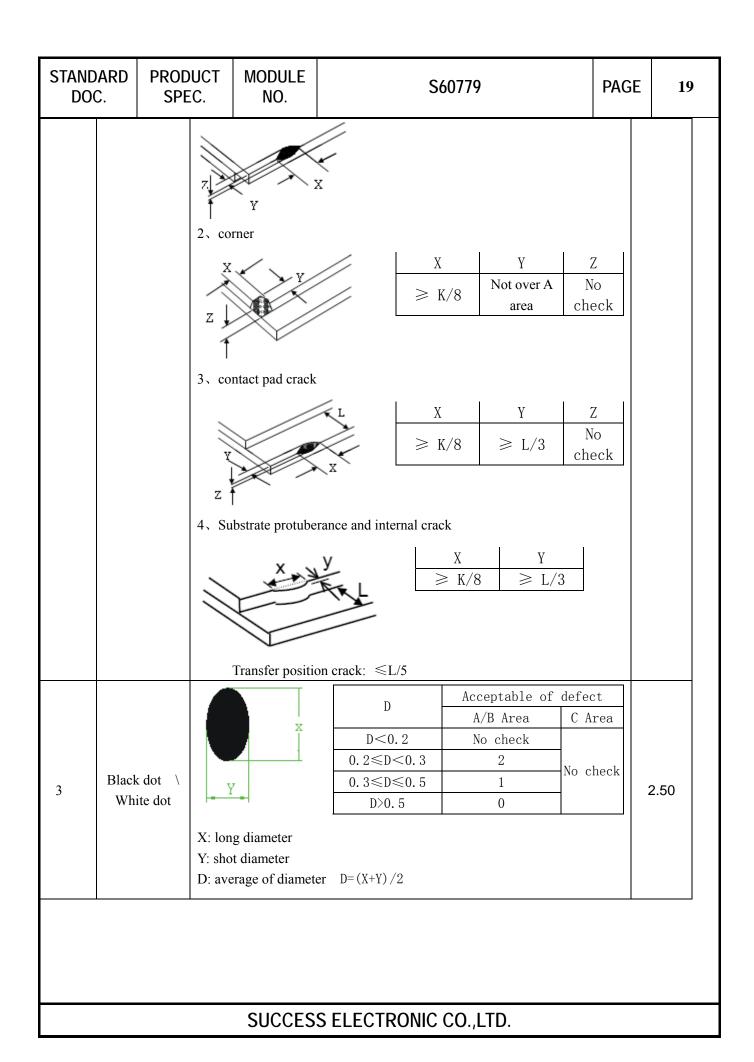
19-1 RELIABILITY TEST

Test item	Test condition	Number of failures/number of examinations		
高温储存 High temperature	70 °C 240hrs	0/8		
storage				
低温储存 Low temperature storage	-20 °C 240hrs	0/8		
高温高湿储存 High	55 °C,95% 240hrs(极限条件)	0/8		
temperature/humidity storage				
高温工作 High temperature	60 °C 240hrs	0/10		
operation				
低温工作 Low temperature	-10 °C 240hrs	0/10		
operation				
高温高湿工作 High	55°C,95% 168hrs(极限条件)	0/10		
temperature/humidity operation				
热冲击 Thermal shock	-20~70 °C(30min/30min) 10 cycle	0/8		
Electostatic discharge test	Air and Contact 330 ohm 150pF +/-8000V 10 times	0/3		
振动 Vibration test	Amplitude 1.5mm,f=10 to 55 Hz, 2 hours each in the X,Y and Z direction	0/3		
冲击 Impact test	Apply 1g for operation time 6ms, 3 times each in X,Y and Z direction	0/3		
Packing vibration-proof test	2g, f=10->55->10Hz apply in each of X, Y, and Z direction for 30 min	0/3		
Packing drop test	Drop the packing from 75cm height, 3 times for 6-faces, 3-edges and 1-corner			

### 20. THE STANDARD OF INSPECTION

20-1 Inspection items and specification for appearance (power off)

No.	Item	Criterion					
1	Dimension	Dimension out of the specification					
2	Glass crack	1. General crack	X	Y Not over A	Z	2.50	
			≥ K/8	area	≤T		



STAND		PROD SPE		MODULE NO.		S60779 PAGE				20
4	Line d	efect				Whidth  W≤0.02  W≤0.05  W≤0.05  W>0.05	Acceptable A/B Area No check 2 2 As ro	No c	rea heck	2.50
5	Polar Bubl		Y	X	D		Acceptable A/B Area No check 3 2 0	of defe C Ar No ch	ea	2.50
6	External of panel	-		<ol> <li>Transfigure, pin hole: same as segment transfinguer</li> <li>Print width: print width ≥1/2 standard width is acceptable</li> </ol>						
7	Silicon	glue	The area of painting silicon glue must cover the ITO circuit.							2.50
8	Defect PCB	of	<ol> <li>The char wrong edition bresking off circuit crack and air-logged orifice are unreceivable for PCB.</li> <li>gold finger of PCB can not be oxidative smudgy and broken.</li> </ol>						ogged	2.50
9	SMT or	gan	<ol> <li>deflexion of component≤1/3width of component</li> <li>Trying to keep dot of soldering tin orbicular</li> <li>Damage \( \text{break} \) wrong assembly and unseal are unreceivable for component.</li> </ol>						le for	2.50
10	Steel Fr	ame	<ol> <li>Break and distortion are unreceivable for frame.</li> <li>If there is one nick which can not lead to cast or hole of painting, we allow that following:     Length≤5mm;Width≤0.3mm</li> </ol>					ig, we	2.50	

STAND DO		PRODUCT SPEC.	MODU NO			S60779			PAGE	2
20-2 In:	spection it	ems and spec	cification for	or display d	efect	(power on)				
	E1	1	Se	egment mis	sing	Not all	OW			
1	Electri		<del>-</del>	Segment sh		Not all	OW			1.0
	Defe	ct		Non-displ	ay	Not all	OW			
		1, P	in hole				1			
				٠ ـ ـ ـ ـ ـ ـ ـ ـ ـ ـ ـ ـ ـ ـ ـ ـ ـ ـ ـ		width	Acceptable			
			D (S)	3 T D		W<0.4	D≤0.2 &	D≤1/2	2W	
2	Pin ho	ala 👤		₹ B		W≥0.4	D≤0.25 &	b D≤1/3		2.50
		<i>A</i>	W		* [		≤0.1 accep			
			<u> </u>	to II-F		Width	Acceptable			
						W<0.4	C, D, (			
3	Displopatter					W≥0.4	C, D,	G≤0.2	1.0	
		W: D	Design dime	ension C	, D:	discrepant dimens	sion G= E-F Acceptab			
			X X		x	D	A/B Area	C Are	ea	
						D<0.1	No check			
4	Black/white dot	hite		1		0. 1≤D<0. 2	2	No check	eck	0.50
4			Y		0	. 2≤D≤0. 25	1			2.50
		<b>V.</b> 1	-			D>0. 25	0			
		ong diamete ot diamete								
			verage dian		= (X+Y	7) /2				
		D. a.	Tage dian				Accepta	hle OTV	y I	
	Line defect	I A T	Lengt	th	Width	A/B Area	1			
			L	不计	-	W≤0.02	No check	+		
			*	L≤3		W≤0.03	2	No che	eck	
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STANDARD DOC.	PRODUCT SPEC.	MODULE NO.	S60779	PAGE	22
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#### 21. USING LCD MODULES

#### 21-1 LIQUID CRYSTAL DISPLAY MODULES

LCD is composed of glass and polarizer. Pay attention to the following items when handling.

- (1) Please keep the temperature within specified range for use and storage. Polarization degradation, bubble generation or polarizer peel-off may occur with high temperature and high humidity.
- (2) Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.).
- (3) N-hexane is recommended for cleaning the adhesives used to attach front/rear polarizers and reflectors made of organic substances which will be damaged by chemicals such as acetone, toluene, ethanol and isopropylalcohol.
- (4) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, wipe gently with absorbent cotton or other soft material like chamois soaked in Isopropyl alcohol or Ethyl alcohol. Do not scrub hard to avoid damaging the display surface.
- (5) Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading.
- (6) Avoid contacting oil and fats.
- (7) Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizers. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- (8) Do not put or attach anything on the display area to avoid leaving marks on.
- (9) Do not touch the display with bare hands. This will stain the display area and degradate insulation between terminals (some cosmetics are determinated to the polarizers).
- (10) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- (11) As glass is fragile. It tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring.

#### 21-2 PRECAUTION FOR HANDING LCD MODULES

Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.

- (1) Do not alter, modify or change the the shape of the tab on the metal frame.
- (2) Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- (3) Do not damage or modify the pattern writing on the printed circuit board.
- (4) Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal connector.
- (5) Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- (6) Do not drop, bend or twist LCM. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- (7) In order to avoid the cracking of the FPC, you should to pay attention to the area of FPC where the FPC was bent .the edge of coverlay; the area of surface of Ni-Au plating, the area of soldering land, the area of through hole.

STANDARD DOC.	PRODUCT SPEC.	MODULE NO.	S60779	PAGE	23
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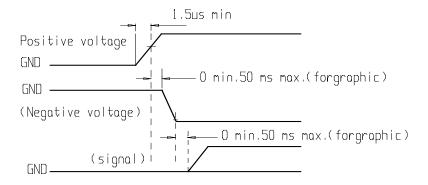
#### 21-3 ELECTRO-STATIC DISCHARGE CONTROL

Since this module uses a CMOS LSI, the same careful attention should be paid to electrostatic discharge as for an ordinary CMOS IC.

- (1) Make certain that you are grounded when handing LCM. To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules. Exposed area of the printed circuit board. Terminal electrode sections.
- (2) Before remove LCM from its packing case or incorporating it into a set, be sure the module and your body have the same electric potential.
- (3) When soldering the terminal of LCM, make certain the AC power source for the soldering iron does not leak.
- (4) When using an electric screwdriver to attach LCM, the screwdriver should be of ground potentiality to minimize as much as possible any transmission of electromagnetic waves produced sparks coming from the commutator of the motor.
- (5) As far as possible make the electric potential of your work clothes and that of the work bench the ground potential.
- (6) To reduce the generation of static electricity be careful that the air in the work is not too dried. A relative humidity of 50%-60% is recommended.

#### 21-4 PRECAUTIONS FOR OPERATION

- (1) Viewing angle varies with the change of liquid crystal driving voltage (VO). Adjust VO to show the best contrast.
- (2) Driving the LCD in the voltage above the limit shortens its life.
- (3) If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
- (4) Response time is greatly delayed at temperature below the operating temperature range. However, this does not mean the LCD will be out of the order. It will recover when it returns to the specified temperature range.
- (5) If the display area is pushed hard during operation, the display will become abnormal. However, it will return to normal if it is turned off and then back on.
- (6) Condensation on terminals can cause an electrochemical reaction disrupting the terminal circuit. Therefore, it must be used under the relative condition of 40°C, 50% RH.
- (7) When turning the power on, input each signal after the positive/negative voltage becomes stable.



#### 21-5 STORAGE

STANDARD DOC.	PRODUCT SPEC.	MODULE NO.	S60779	PAGE	24
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When storing LCDs as spares for some years, the following precaution are necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C.
- 3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped.)
- (4) Environmental conditions:
  - Do not leave them for more than 160hrs. at 70°C.
  - Should not be left for more than 48hrs. at -20°C.

#### 21-6 SAFETY

- (1) It is recommended to crush damaged or unnecessary LCDs into pieces and wash them off with solvents such as acetone and ethanol, which should later be burned.
- (2) If any liquid leakes out of a damaged glass cell and comes in contact with the hands, wash off thoroughly with soap and water.

#### 21-7 LIMITED WARRANTY

Unless agreed between SUCCESS and customer, SUCCESS will replace or repair any of its LCD modules which are found to be functionally defective when inspected in accordance with SUCCESS LCD acceptance standards (copies available upon request) for a period of one year from date of shipments. Cosmetic/visual defects must be returned to SUCCESS within 90 days of shipment. Confirmation of such date shall be based on freight documents. The warranty liability of SUCCESS limited to repair and/or replacement on the terms set forth above. SUCCESS will not be responsible for any subsequent or consequential events.

#### 21-8 RETURN LCM UNDER WARRANTY

No warranty can be granted if the precautions stated above have been disregarded. The typical examples of violations are:

- Broken LCD glass.
- Circuit modified in any way, including addition of components.

Module repairs will be invoiced to the customer upon mutual agreement. Modules must be returned with sufficient description of the failures or defects. Any connectors or cable installed by the customer must be removed completely without damaging the PCB's eyelet, conductors and terminals.